

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Averett, Guy E. et al.

Serial No.: 10/072,145

Filed: 02/07/2002

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Examining Group: 2811  
Examiner: Ori Nadiv

For: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF  
LOW SUBSTRATE CAPACITANCE

**AMENDMENT**

Honorable Commissioner of Patents,  
P.O. BOX 1450  
Alexandria, VA 22313-1450

SIR:

In response to the office action mailed August 16, 2006, please reconsider the above-identified application in view of the amendments and remarks presented hereinafter.